EMI PROTECTIVE COVER MOUNTING ARRANGEMENT

ABSTRACT OF THE DISCLOSURE

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An EMI protective cover is constructed to include a bottom cover shell for covering on the circuit board over electronic components, the bottom cover shell having stub pins around the four sides and an inspection hole in the top wall, and a top cover shell detachably covered on the bottom cover shell, the top cover shell having springy mounting lugs and smoothly arched springy retaining lugs arranged around the four sides and respectively pressed on the peripheral walls of the bottom cover shell, the springy mounting lugs each having a pin hole respectively forced into engagement with the stub pins of the bottom cover shell. The electronic components are accessible through the inspection hole after removal of the top cover shell from the bottom cover shell.